

50-1296 MHz Linear Gain Blocks WA3JUF 1987

Gain: 50 = 18-20dB

144 = 18-20 dB

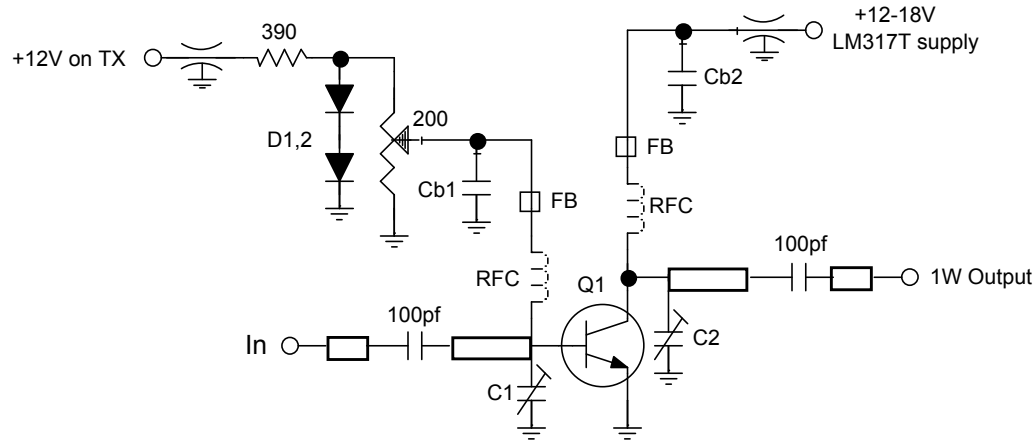
220 = 17 dB

432 = 15 dB

903 = 13 dB

1296 = 10 dB

Adjust bias for $I_{CQ}=20-50mA$



Q1 - SD1520, SD1598

C1 - (50-432) 10-20pf (903-1296) 2-6pf

C2 - (50-432) 10-20pf (903-1296) not used

Cb1 - 100pf chip + .001ufd + .01ufd

Cb2 - 100pf chip + .01ufd + 1ufd tant

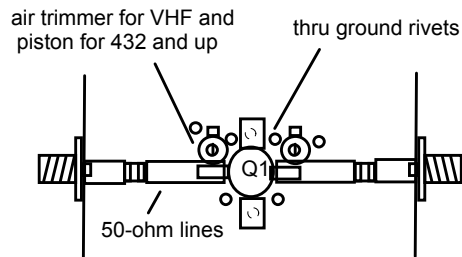
D1,2 - 1N4001 glued to lid of Q1

RFC - (50) 12t #28, 0.1" ID or Z-50

RFC - (144-432) 12t, #24, 0.1" ID

RFC - (903) 8t, #24, 0.1" ID

RFC - (1296) 6t, #24, 0.1" ID



SMA connectors soldered to both sides of the PC board, which can be part of the box.

Cascade no more than 2 stages in one box.

The SD1520, SD1598 is like a UHF version of a 2N3866 in a stripline package.