ELECTRICAL CHARACTERISTICS — continued (T_A = 25°C unless otherwise noted, V_F = 1.2 Max @ I_F = 1 A for all types)

JEDEC Type No. (Note 1)	Nominal Zener Voltage V_Z @ I_{ZT} Volts (Note 2)	Test Current I_{ZT} mA	Max Zener Impedance		Max Reverse Leakage Current		Max Surge Current i_r, Amps (Note 3)	Max Voltage Regulation ΔV_Z, Volt (Note 4)	Maximum Regulator Current I_{ZM} mA (Note 5)
			Z_{ZT} @ I_{ZT} Ohms (Note 2)	Z_{ZK} @ I_{ZK} = 1 mA Ohms (Note 2)	I_R μA	@ V_R Volts			
1N5383B	***150***	***8***	***330***	***1500***	***0.5***	***114***	***1.1***	***3***	***31.6***
1N5384B	160	8	350	1650	0.5	122	1.1	3	29.4
1N5385B	170	8	380	1750	0.5	129	1	3	28
1N5386B	180	5	430	1750	0.5	137	1	4	26.4
1N5387B	190	5	450	1850	0.5	144	0.9	5	25
1N5388B	200	5	480	1850	0.5	152	0.9	5	23.6

NOTE 1. TOLERANCE AND TYPE NUMBER DESIGNATION
The JEDEC type numbers shown indicate a tolerance of ±5%.

NOTE 2. ZENER VOLTAGE (V_Z) AND IMPEDANCE (Z_{ZT} & Z_{ZK})

Test conditions for zener voltage and impedance are as follows: I_Z is applied 40 ± 10 ms prior to reading. Mounting contacts are located 3/8″ to 1/2″ from the inside edge of mounting clips to the body of the diode. (T_A = 25°C +8, –2°C).

NOTE 3. SURGE CURRENT (i_r)

Surge current is specified as the maximum allowable peak, non-recurrent square-wave current with a pulse width, PW, of 8.3 ms. The data given in Figure 6 may be used to find the maximum surge current for a square wave of any pulse width between 1ms and 1000 ms by plotting the applicable points on logarithmic paper. Examples of this, using the 3.3 V and 200 V zeners, are shown in Figure 7. Mounting contact located as specified in Note 3. (T_A = 25°C +8, –2°C.)

NOTE 4. VOLTAGE REGULATION (ΔV_Z)

Test conditions for voltage regulation are as follows: V_Z measurements are made at 10% and then at 50% of the I_Z max value listed in the electrical characteristics table. The test current time duration for each V_Z measurement is 40 ± 10 ms. (T_A = 25°C +8, –2°C). Mounting contact located as specified in Note 2.

NOTE 5. MAXIMUM REGULATOR CURRENT (I_{ZM})

The maximum current shown is based on the maximum voltage of a 5% type unit, therefore, it applies only to the B-suffix device. The actual I_{ZM} for any device may not exceed the value of 5 watts divided by the actual V_Z of the device. T_L = 75°C at 3/8″ maximum from the device body.

NOTE 6. SPECIALS AVAILABLE INCLUDE:

Nominal zener voltages between the voltages shown and tighter voltage tolerance such as ±1% and ±2%. Consult factory.

TEMPERATURE COEFFICIENTS

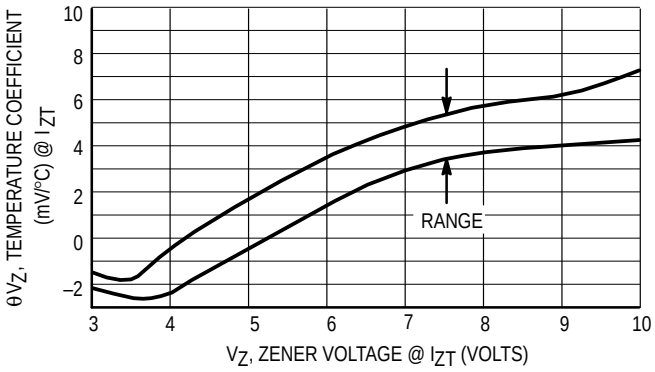


Figure 2. Temperature Coefficient-Range for Units 3 to 10 Volts

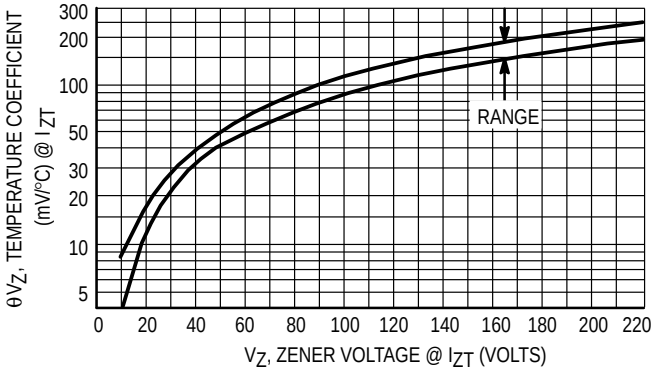


Figure 3. Temperature Coefficient-Range for Units 10 to 220 Volts

Devices listed in bold, italic are Motorola preferred devices.

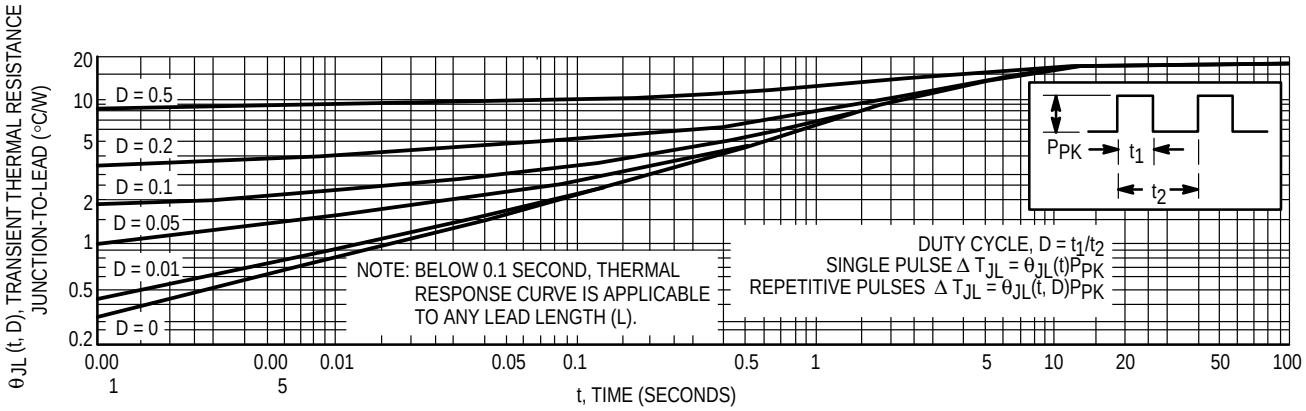


Figure 4. Typical Thermal Response
L, Lead Length = 3/8 Inch

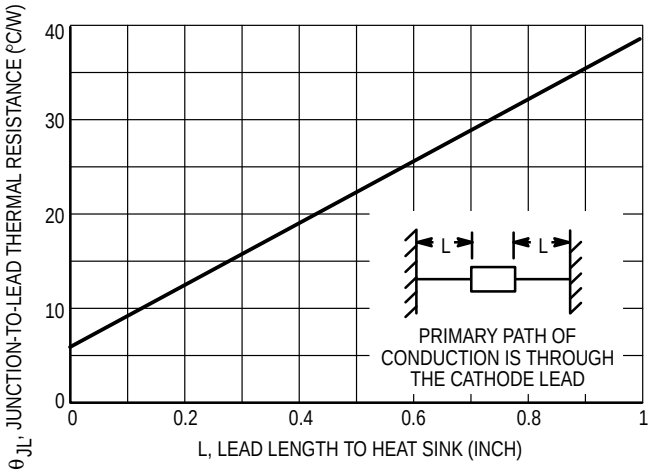


Figure 5. Typical Thermal Resistance

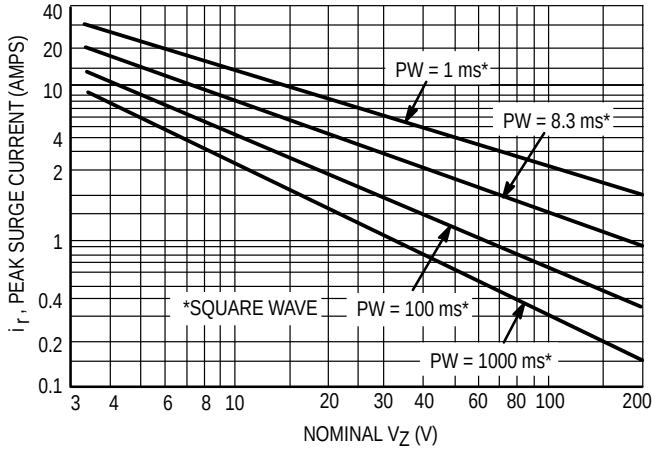


Figure 6. Maximum Non-Repetitive Surge Current versus Nominal Zener Voltage (See Note 3)

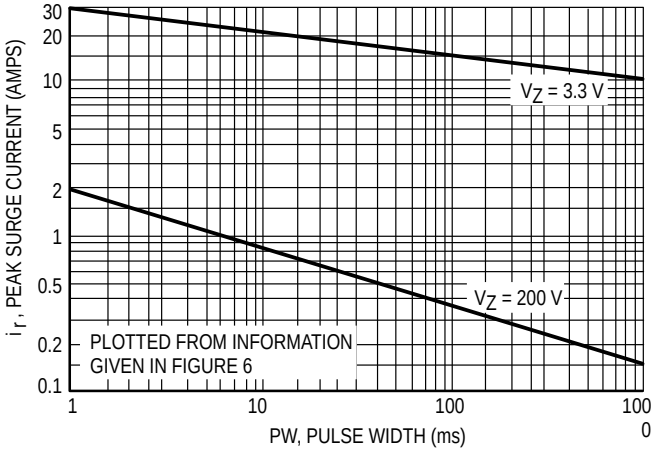


Figure 7. Peak Surge Current versus Pulse Width (See Note 3)

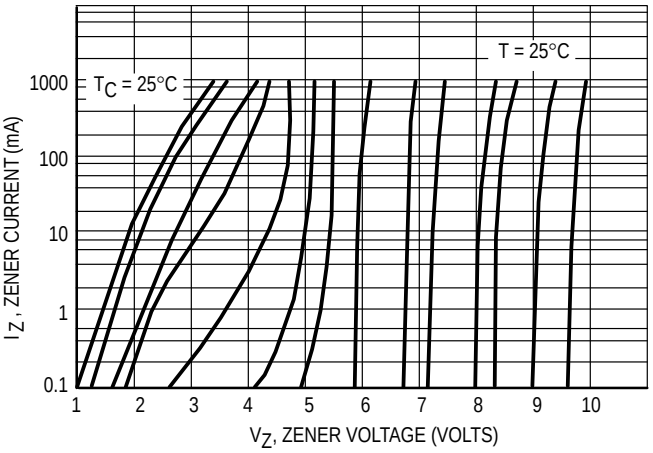


Figure 8. Zener Voltage versus Zener Current VZ = 3.3 thru 10 Volts

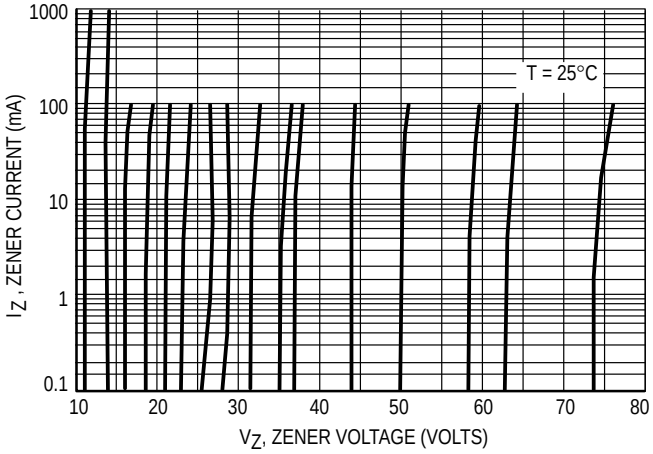


Figure 9. Zener Voltage versus Zener Current
V_Z = 11 thru 75 Volts

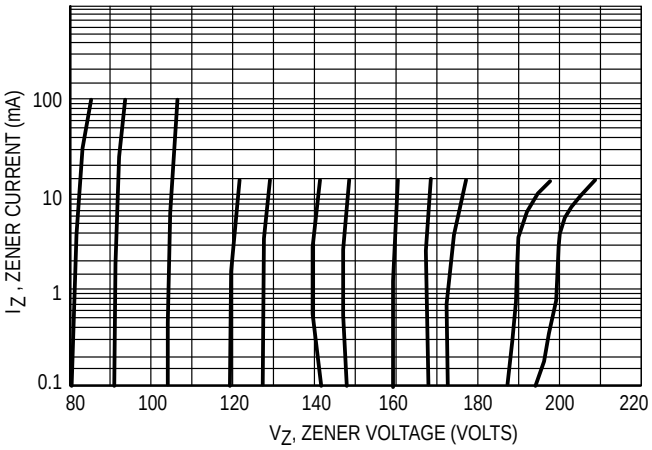


Figure 10. Zener Voltage versus Zener Current
V_Z = 82 thru 200 Volts

APPLICATION NOTE

Since the actual voltage available from a given zener diode is temperature dependent, it is necessary to determine junction temperature under any set of operating conditions in order to calculate its value. The following procedure is recommended:

Lead Temperature, T_L, should be determined from:

$$T_L = \theta_{LA} P_D + T_A$$

θ_{LA} is the lead-to-ambient thermal resistance and P_D is the power dissipation.

Junction Temperature, T_J, may be found from:

$$T_J = T_L + \Delta T_{JL}$$

ΔT_{JL} is the increase in junction temperature above the lead temperature and may be found from Figure 4 for a train of power pulses or from Figure 5 for dc power.

$$\Delta T_{JL} = \theta_{JL} P_D$$

For worst-case design, using expected limits of I_Z, limits of P_D and the extremes of T_J (ΔT_J) may be estimated. Changes in voltage, V_Z, can then be found from:

$$\Delta V = \theta_{VZ} \Delta T_J$$

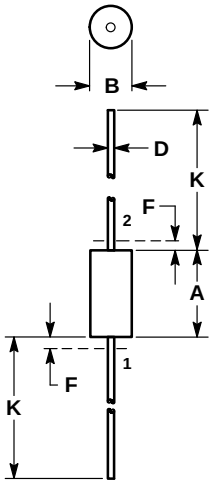
θ_{VZ}, the zener voltage temperature coefficient, is found from Figures 2 and 3.

Under high power-pulse operation, the zener voltage will vary with time and may also be affected significantly by the zener resistance. For best regulation, keep current excursions as low as possible.

Data of Figure 4 should not be used to compute surge capability. Surge limitations are given in Figure 6. They are lower than would be expected by considering only junction temperature, as current crowding effects cause temperatures to be extremely high in small spots resulting in device degradation should the limits of Figure 6 be exceeded.

Zener Voltage Regulator Diodes — Axial Leaded

5 Watt Surmetic 40



NOTE:
1. LEAD DIAMETER & FINISH NOT CONTROLLED WITHIN DIM F.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.330	0.350	8.38	8.89
B	0.130	0.145	3.30	3.68
D	0.037	0.043	0.94	1.09
F	—	0.050	—	1.27
K	1.000	1.250	25.40	31.75

CASE 17-02
PLASTIC

(Refer to Section 10 for Surface Mount, Thermal Data and Footprint Information.)

MULTIPLE PACKAGE QUANTITY (MPQ) REQUIREMENTS

Package Option	Type No. Suffix	MPQ (Units)
Tape and Reel	RL	4K
Tape and Ammo	TA	2K

(Refer to Section 10 for more information on Packaging Specifications.)

Devices listed in bold, italic are Motorola preferred devices.