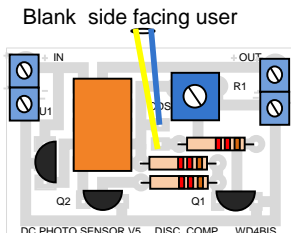
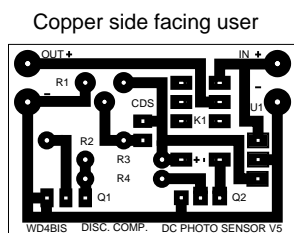
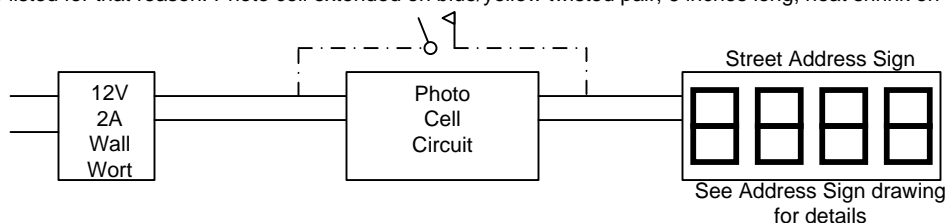
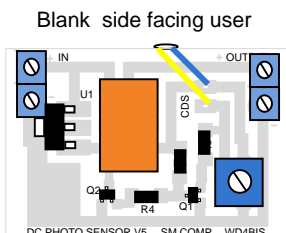
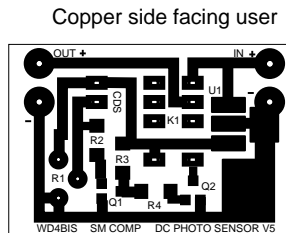


Purchased a grab bag of 50 CDS cells from Jameco. Various Values. All cells worked with bias adjustment pot. No value listed for that reason. Photo cell extended on blue/yellow twisted pair, 6 inches long, heat shrink on leads.



Discrete Components PCB and parts placement



Surface Mount Components PCB and parts placement. Blue and orange components mounted on blank side of board. Black components mounted on copper side of board.

WD4BIS typical Color Code-Point to Point wiring
Black- Ground
Red- Highest DC Voltage
Blue- 5VDC
Yellow- Interconnect
White- Data or Address bus

Date	Revision/Addition/ Note	By:
Dec 22, 2015	Initial Drawing. Prototype worked on bread board.	GSC
Dec 23, 2015	Working prototype, added optional bypass switch to drawing. Changed 78L05 to "78L05 or 78M05". Have 78M05 in stock Surface Mount (SM).	GSC
Jan 28, 2016	Built 3 rd prototype but used 2N3904 transistors. Worked.	GSC
Jan 28, 2016	Surplus relay Axiom V23079-A1011-B301 is polarized. EC2-5NU is polarized.	GSC
Feb 2, 2016	Built one with surface mount components. Used MMBT3904 transistors and 1K resistors rather than 1.2K as I had them in SM. Worked.	GSC
Feb 23, 2016	Version 4 PWB Artwork. SM version worked. Discreet version had dimensional issues. Scraped these boards.	GSC
Mar 14, 2016	Version 5 PWB Artwork proved out, both SM and Discreet.	GSC
Drawn By:	Gerald Crenshaw WD4BIS	Date: Dec 22, 2015
Designed By:	Gerald Crenshaw WD4BIS	Date: Dec 22, 2015
Checked By:	Janet Crenshaw WB9ZPH	Date: Dec 22, 2015

Package Outlines

